

Taleigao Plateau, Goa 403 206

RESULT REGISTER FOR M.E INDUSTRIAL ENGINEERING SEMESTER - I EXAMINATION HELD IN MAY 2016

Course : Revised Course - 2013

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COLLEGE : GOA COLLEGE OF ENGINEERING	G			- 15		
Seat No: 7101 P R No: 201408857	Sex	<: M	Nam	e: E	DWIN MONTE CRISTO VAZ	
No Of Attempts : 4		No Of Credits	Grad		SGPA	
Principles of Industrial Engineering and	Managemen					
	Theory	4	FF	F		
	IA	1	BC	+		
	Oral	1	BC	+		
Engineering Statistics						
	Theory	4	FF	F		
	IA	1	CC	+		
	Oral	1	CC	+		
Seminar I						
	IA	2	BC	+		
	Oral	4	CC	+		
	Total :	18			3.00 F	
	rotar.	10			FAILS	
Seat No : 7102 P R No : 152107007	Sex	<: M	Nam	e: H	ANAGI GORAKSHANATH	
No Of Attempts: 2		No Of	Grad			
		Credits	Obtai		SGPA	
Principles of Industrial Engineering and	Managemen		o o tan	i o a		
	Theory	4	CC	+		
	IA	1	BC	+		
	Oral	1	BC	+		
Engineering Statistics						
	Theory	4	BC	+		
	IA	1	BB	+		
	Oral	1	FF	F		
Seminar I	oru					
ooninidi 1	IA	2	CC	+		
	Oral	4	CC	+		
	Total :	18			5.17 F	
	TOLAT.	10			FAILS	
Seat No : 7103 P R No : 201406583	Sex	<: M	Nam	e: N	AIK SANJAY SONU	
No Of Attempts: 4		No Of	Grad			
		Credits	Obtai		SGPA	
Principles of Industrial Engineering and	Management		obtai	lou		
	Theory	4	FF	F		
	IA	1	BC	+		
	Oral	1	CC	+		
Engineering Statistics						
	Theory	4	FF	F		
	IA	1	CC	+		
	Oral	1	CC	+		
Seminar I			00			
oonina i	IA	2	BC	+		
	Oral	4	CC	+		
•			00			
	Total :	18			2.94 F	

P: Passes; F: Fails; A/ABS: Absent; N/NAP: Non Appearance; X/NE: Not Eligible; +: Grades Carried Over; SGPA: Semester Grade Point Average; CGPA: Cummulative Grade Point Average

Discrepancy, if any in the Result shall be informed to the University within a week from the date of receipt of the result by the college

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Taleigao Plateau, Goa 403 206

RESULT REGISTER FOR M.E INDUSTRIAL ENGINEERING SEMESTER - I EXAMINATION HELD IN MAY 2016

COLLEGE : GOA COLLEGE OF ENGINEERING Seat No: 7104 P R No: 201507398 Name: RANE VAMANRAO UDAY Sex: M No Of Attempts: 2 No Of Grade SGPA Credits Obtained Principles of Industrial Engineering and Management Theory 4 CC + IA BC 1 + Oral CC 1 + **Engineering Statistics** Theory 4 CC Ρ IA BC 1 + Oral FF 1 F Seminar I IA 2 CC + Oral 4 CC + Total: 18 4.83 F FAILS Seat No: 7105 SAWANT SHIRISH PURSHOTTAM P R No: 201004841 Sex: M Name : No Of Attempts: 2 No Of Grade SGPA Credits Obtained Principles of Industrial Engineering and Management Theory BB 4 + IA CC 1 + Oral BC 1 + **Engineering Statistics** Theory 4 AB IA AA 1 + Oral AB Ρ 1 Seminar I IA 2 CC Oral 4 CC + Total : 18 6.56 P

-		PRODUCT OF NAMES AND ADDRESS A	consecutor we to be out the set of the other of the set	C IN CONTRACTOR OF
	Grade	Grade Points	Performance	Read By : pohelle
	AO	10	Outstanding	Checked By : Ku
	AA	9	Excellent	Checked by .
	AB	8	Very Good	Date 27/7/16
	BB	7	Good	Date 24/7/10
	BC	6	Fair	
	CC	5	Satisfactory	
	FF	0	Fail	

26/7/2016 Leo V. Macedo

ALEIGAO ATFAU Ans

PASSES

Figueiredo Controller Of Examinations

eddy Y. V. Reddy Registrar

Course : Revised Course - 2013

SSI Assistant Registrar-E(Proff.)

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Taleigao Plateau, Goa 403 206

RESULT REGISTER FOR M.E FOUNDATION ENGINEERING SEMESTER - I EXAMINATION HELD IN MAY 2016

Course : Revised Course - 2013

Seat No : 1101 P R No : 201507392	Se	ex: M	Nam	ne: A	SHRAF NEHAL ABDUS SAMI
No Of Attempts : 2		No Of Credits	Grad		SGPA
Advanced Soil Mechanics					
	Theory	4	BC	+	
	IA	2	BB	+	
Shallow Foundations	-				
	Theory	4	ABS		
Rock Mechanics	IA	2	CC	+	
Rock Mechanics	Theony	4	DD		
	Theory	4	BB BC	+	
Ground Improvement Techniques	IA	2	BC	+	
Ground improvement rechniques	Theory	4	BB	+	
	IA	2	BB	+	
Engineering Geology		2	DD	1	
Engineering Geology	Theory	4	BC	+	
	IA	2	CC	+	
Laboratory I	17 (	-	00		
	IA	2	AB	+	
	Oral	2	AB	+	
	Total:	34			5.76 F
	rotar.	0,			FAILS
eat No: 1102 P R No: 201004786	Se	ex: M	Nam	e: D	ESSAI ANIKET RAJU
lo Of Attempts: 3		No Of	Grad	de	
		Credits	Obtai	ned	SGPA
Advanced Soil Mechanics					
	Theory	4	BC	+	
	IA	2	AB	+	
Shallow Foundations					
Shallow Foundations			00		
Shallow Foundations	Theory	4	CC	Ρ	
	Theory IA	4 2	CC CC	P +	
Rock Mechanics	IA	2	СС	+	
	IA Theory	2 4	cc cc	+	
Rock Mechanics	IA	2	СС	+	•
	IA Theory IA	2 4 2	CC CC BC	+ + +	•
Rock Mechanics	IA Theory IA Theory	2 4 2 4	CC CC BC BC	+ + +	•
Rock Mechanics Ground Improvement Techniques	IA Theory IA	2 4 2	CC CC BC	+ + +	•
Rock Mechanics	IA Theory IA Theory IA	2 4 2 4 2	CC CC BC BC AB	+ + + + +	
Rock Mechanics Ground Improvement Techniques	IA Theory IA Theory IA	2 4 2 4 2 4	CC BC BC AB CC	+ + +	
Rock Mechanics Ground Improvement Techniques Engineering Geology	IA Theory IA Theory IA	2 4 2 4 2	CC BC BC AB CC	+ + + + + +	
Rock Mechanics Ground Improvement Techniques	IA Theory IA Theory IA Theory IA	2 4 2 4 2 4 2	CC BC BC AB CC CC	+ + + + + +	
Rock Mechanics Ground Improvement Techniques Engineering Geology	IA Theory IA Theory IA	2 4 2 4 2 4	CC BC BC AB CC CC BB	+ + + + + + +	

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Taleigao Plateau, Goa 403 206

RESULT REGISTER FOR M.E FOUNDATION ENGINEERING SEMESTER - I EXAMINATION HELD IN MAY 2016

Course : Revised Course - 2013

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Prof. Y. V. Reddy

Registrar

COLLEGE :	GOA COLLEGE	OF ENGINEERING

Seat No : 1103 P R No : 201006500	S	ex: M	Nam	ne :	PARAB PARESH DAMODAR	
No Of Attempts : 3		No Of Credits	Grad Obtai		SGPA	
Advanced Soil Mechanics						
	Theory	4	BC	+		
	IA	2	AB	+		
Shallow Foundations						
	Theory	4	CC	Ρ		
	IA	2	CC	+		
Rock Mechanics						
	Theory	4	CC	+		
	IA	2	AB	+		
Ground Improvement Techniques						
	Theory	4	BC	+		
	IA	2	BB	+		
Engineering Geology						
	Theory	4	CC	+		
	IA	2	CC	+		
Laboratory I						
	IA	2	BC	+		
	Oral	2	BC	+		
	Total :	34	*******		5.82 P PASSES	

Grade Grade Performance Points Read By : pondu KH AO 10 Outstanding Checked By AA 9 Excellent AB 8 Very Good 716 Date: 27 BB 7 Good BC Fair 6 CC 5 Satisfactory FF 0 Fail

26/7/2016 S.S.J. Figueiredo

Assistant Registrar-E(Proff.)

Leo V. Macedo Controller Of Examinations



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## GOA UNIVERSITY

Taleigao Plateau, Goa 403 206

RESULT REGISTER FOR M.E ELECTRONICS & TELECOMMUNICATION (MICROELECTRONICS) SEMESTER - I EXAMINATION HELD IN MAY 2016 Course : Revised Course - 2013

t No : 3101 P R No : 201108381	Sex	x:F	Nam	e: GO	OVEKAR DIV	YA DIGAMBAF	2
Of Attempts: 2		No Of Credits	Grad		SGPA		
Solid State Devices and Semiconductor	Physics						
	Theory	4	CC	+	*		
	IA	2	BB	+			
Digital VLSI Design							
	Theory	4	BB	Ρ			
	IA	2	CC	+			
Analog VLSI Design							
	Theory	4	CC	+			
· · ·	IA	2	BB	+			
VLSI Technology	_						
	Theory	4	CC	+			
	IA	2	BC	+			
Advanced Engineering Mathematics	The		~~~				
	Theory	4	CC	+			
	IA	2	BC	+			
HDL - LAB		2					
	IA Practical	2	AA	+			
VLSI CAD - LAB	Practical	2	AB	+			
VLSI CAD - LAB	IA	2	BB	+			
	Practical	2 2	BC	+			
	Flactical	2	BC	т			
	Tatal	20			COF D		
	Total :	38		***	6.05 P PASSES		
t No : 3102 P R No : 201004714			Nam	e: KA	PASSES		I
t No : 3102 P R No : 201004714 Of Attempts : 3		k: F			PASSES	DDHA SABAJ	Ι
t No : 3102 P R No : 201004714 Of Attempts : 3		k : F No Of	Grad	de	PASSES		I .
	Se	k: F		de	PASSES		I .
Of Attempts: 3	Se	k : F No Of	Grad	de	PASSES		Ι
Of Attempts: 3	Sex Physics	No Of Credits	Grad Obtai CC	de ned	PASSES		Г.,
Of Attempts: 3	Sex Physics Theory	k: F No Of Credits 4	Grad Obtai CC	de ned +	PASSES		Γ, . ·
Of Attempts : 3 Solid State Devices and Semiconductor	Sex Physics Theory	k: F No Of Credits 4	Grad Obtai CC	de ned +	PASSES		Γ, .
Of Attempts : 3 Solid State Devices and Semiconductor	Sex Physics Theory IA	K: F No Of Credits 4 2	Grad Obtai CC AA	de ned + +	PASSES		Ι, .
Of Attempts : 3 Solid State Devices and Semiconductor	Sex Physics Theory IA Theory	K: F No Of Credits 4 2 4	Grad Obtai CC AA BC	de ned + +	PASSES		Ι, .
Of Attempts : 3 Solid State Devices and Semiconductor Digital VLSI Design	Sey Physics Theory IA Theory IA Theory	x: F No Of Credits 4 2 4 2 4 2 4	Grad Obtai CC AA BC BB BC	de ned + +	PASSES		Ι,
Of Attempts: 3 Solid State Devices and Semiconductor Digital VLSI Design Analog VLSI Design	Sey Physics Theory IA Theory IA	x: F No Of Credits 4 2 4 2	Grad Obtai CC AA BC BB	de ned + + +	PASSES		Ι,
Of Attempts : 3 Solid State Devices and Semiconductor Digital VLSI Design	Sey Physics Theory IA Theory IA Theory IA	x: F No Of Credits 4 2 4 2 4 2 4	Grad Obtai CC AA BC BB BC BB	de ned + + + + +	PASSES		Ι,
Of Attempts: 3 Solid State Devices and Semiconductor Digital VLSI Design Analog VLSI Design	Sex Physics Theory IA Theory IA Theory IA Theory	K: F No Of Credits 4 2 4 2 4 2 4 2 4 2 4	Grad Obtai CC AA BC BB BC BB CC	de ned + + + + +	PASSES		
Of Attempts : 3 Solid State Devices and Semiconductor Digital VLSI Design Analog VLSI Design VLSI Technology	Sey Physics Theory IA Theory IA Theory IA	K: F No Of Credits 4 2 4 2 4 2 4 2	Grad Obtai CC AA BC BB BC BB	de ned + + + + + + +	PASSES		
Of Attempts: 3 Solid State Devices and Semiconductor Digital VLSI Design Analog VLSI Design	Sex Physics Theory IA Theory IA Theory IA Theory IA	Credits 4 2 4 2 4 2 4 2 4 2 4 2	Grad Obtai CC AA BC BB BC BB CC AB	de ned + + + + + + + + +	PASSES		
Of Attempts : 3 Solid State Devices and Semiconductor Digital VLSI Design Analog VLSI Design VLSI Technology	Sex Physics Theory IA Theory IA Theory IA Theory IA Theory	Credits 4 2 4 2 4 2 4 2 4 2 4 2 4 2 4	Grad Obtai CC AA BC BB BC BB CC AB CC	de ned + + + + + + + + P	PASSES		
Of Attempts : 3 Solid State Devices and Semiconductor Digital VLSI Design Analog VLSI Design VLSI Technology Advanced Engineering Mathematics	Sex Physics Theory IA Theory IA Theory IA Theory IA	Credits 4 2 4 2 4 2 4 2 4 2 4 2	Grad Obtai CC AA BC BB BC BB CC AB	de ned + + + + + + + + +	PASSES		
Of Attempts : 3 Solid State Devices and Semiconductor Digital VLSI Design Analog VLSI Design VLSI Technology	Sex Physics Theory IA Theory IA Theory IA Theory IA Theory IA	Credits 4 2 4 2 4 2 4 2 4 2 4 2 4 2 4 2 4 2	Grad Obtai CC AA BC BB BC BB CC AB CC CC	de ned + + + + + + + + + + +	PASSES		
Of Attempts : 3 Solid State Devices and Semiconductor Digital VLSI Design Analog VLSI Design VLSI Technology Advanced Engineering Mathematics	Sex Physics Theory IA Theory IA Theory IA Theory IA Theory IA	<ul> <li>F</li> <li>No Of Credits</li> <li>4</li> <li>2</li> <li>4</li> <li>4</li> <li>2</li> <li>4</li> <li>4</li></ul>	Grad Obtai CC AA BC BB BC BB CC AB CC CC AA	de ned + + + + + + + + + + +	PASSES		
Of Attempts : 3 Solid State Devices and Semiconductor Digital VLSI Design Analog VLSI Design VLSI Technology Advanced Engineering Mathematics HDL - LAB	Sex Physics Theory IA Theory IA Theory IA Theory IA Theory IA	Credits 4 2 4 2 4 2 4 2 4 2 4 2 4 2 4 2 4 2	Grad Obtai CC AA BC BB BC BB CC AB CC CC	de ned + + + + + + + + + + +	PASSES		
Of Attempts : 3 Solid State Devices and Semiconductor Digital VLSI Design Analog VLSI Design VLSI Technology Advanced Engineering Mathematics	Sey Physics Theory IA Theory IA Theory IA Theory IA Theory IA IA Practical	<ul> <li>K: F</li> <li>No Of Credits</li> <li>4</li> <li>2</li> <li>4</li> <l< td=""><td>Grad Obtai CC AA BC BB BC BB CC AB CC CC AA AA</td><td>de ned + + + + + + + + + + + + +</td><td>PASSES</td><td></td><td></td></l<></ul>	Grad Obtai CC AA BC BB BC BB CC AB CC CC AA AA	de ned + + + + + + + + + + + + +	PASSES		
Of Attempts : 3 Solid State Devices and Semiconductor Digital VLSI Design Analog VLSI Design VLSI Technology Advanced Engineering Mathematics HDL - LAB	Sey Physics Theory IA Theory IA Theory IA Theory IA IA Practical IA	<ul> <li>F</li> <li>No Of Credits</li> <li>4</li> <li>2</li> <li>4</li> <li>4</li></ul>	Grad Obtai CC AA BC BB BC BB CC AB CC CC AA AA AA	de ned + + + + + + + + + + + + + +	PASSES		
Of Attempts : 3 Solid State Devices and Semiconductor Digital VLSI Design Analog VLSI Design VLSI Technology Advanced Engineering Mathematics HDL - LAB	Sey Physics Theory IA Theory IA Theory IA Theory IA Theory IA IA Practical	<ul> <li>K: F</li> <li>No Of Credits</li> <li>4</li> <li>2</li> <li>4</li> <l< td=""><td>Grad Obtai CC AA BC BB BC BB CC AB CC CC AA AA</td><td>de ned + + + + + + + + + + + + +</td><td>PASSES</td><td>DDHA SABAJ</td><td></td></l<></ul>	Grad Obtai CC AA BC BB BC BB CC AB CC CC AA AA	de ned + + + + + + + + + + + + +	PASSES	DDHA SABAJ	

P: Passes; F: Fails; A/ABS: Absent; N/NAP: Non Appearance; X/NE: Not Eligible; +: Grades Carried Over; SGPA: Semester Grade Point Average; CGPA: Cummulative Grade Point Average

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Taleigao Plateau, Goa 403 206

RESULT REGISTER FOR M.E ELECTRONICS & TELECOMMUNICATION (MICROELECTRONICS) SEMESTER - I EXAMINATION HELD IN MAY 2016 Course : Revised Course - 2013

eat No : 3103 P R No : 201007353	Sex	k: F	Nam	e: N	MAYEKAR MITHILI NARAYAN	
Of Attempts: 2		No Of Credits	Grad		SGPA	
Solid State Devices and Semiconductor	Physics					
	Theory	4	BC	+		
	IA	2	BC	+		
Digital VLSI Design						
	Theory	4	CC	+		
	IA	2	CC	+		
Analog VLSI Design						
	Theory	4	CC	+		
	IA	2	BC	+		
VLSI Technology						
	Theory	4	CC	+		
	IA	2	CC	+		
Advanced Engineering Mathematics						
	Theory	4	FF	F		
	IA	2	CC	+		
HDL - LAB						
	IA	2	AB	+		
· · · · · · · · · · · · · · · · · · ·	Practical	2	BB	+		
VLSI CAD - LAB						
	IA	2	BC	+		
	Practical	2	CC	+		
		International content of the second				
	Total :	38			5.00 F FAIL S	
at No: 3104 P R No: 201301154			Nam	e: N	FAILS	
eat No : 3104 P R No : 201301154 Of Attempts : 4		<: M			5.00 F FAILS NAIK MILIND MINANATH	
eat No : 3104 P R No : 201301154 Of Attempts : 4		K: M No Of	Grad	de	FAILS	
	Sex	<: M		de	FAILS NAIK MILIND MINANATH	
o Of Attempts : 4	Sex	K: M No Of	Grad	de	FAILS NAIK MILIND MINANATH	
o Of Attempts : 4	Sex Physics	Credits	Grad Obtai	de ned	FAILS NAIK MILIND MINANATH	
Of Attempts: 4	Sex Physics Theory	C: M No Of Credits 4	Grad Obtai BC	de ned +	FAILS NAIK MILIND MINANATH	
Of Attempts : 4 Solid State Devices and Semiconductor	Sex Physics Theory	C: M No Of Credits 4	Grad Obtai BC	de ned +	FAILS NAIK MILIND MINANATH	
Of Attempts : 4 Solid State Devices and Semiconductor	Sex Physics Theory IA	C: M No Of Credits 4 2	Grad Obtai BC CC	de ned + +	FAILS NAIK MILIND MINANATH	
Of Attempts : 4 Solid State Devices and Semiconductor	Sex Physics Theory IA Theory IA	C: M No Of Credits 4 2 4	Grad Obtai BC CC CC	de ned + + +	FAILS NAIK MILIND MINANATH	
Of Attempts: 4 Solid State Devices and Semiconductor Digital VLSI Design	Sex Physics Theory IA Theory	C: M No Of Credits 4 2 4	Grad Obtai BC CC CC AB CC	de ned + + +	FAILS NAIK MILIND MINANATH	
o Of Attempts: 4 Solid State Devices and Semiconductor Digital VLSI Design Analog VLSI Design	Sex Physics Theory IA Theory IA	Credits 4 2 4 2	Grad Obtai BC CC CC AB	de ned + + +	FAILS NAIK MILIND MINANATH	
Of Attempts: 4 Solid State Devices and Semiconductor Digital VLSI Design	Sex Physics Theory IA Theory IA Theory IA	Credits 4 2 4 2 4 2	Grad Obtai BC CC CC AB CC CC	de ned + + + +	FAILS NAIK MILIND MINANATH	
o Of Attempts: 4 Solid State Devices and Semiconductor Digital VLSI Design Analog VLSI Design	Sex Physics Theory IA Theory IA Theory IA Theory	Credits 4 2 4 2 4 2 4 2 4 2 4 2 4	Grad Obtai BC CC CC AB CC CC CC	de ned + + + +	FAILS NAIK MILIND MINANATH	
o Of Attempts: 4 Solid State Devices and Semiconductor Digital VLSI Design Analog VLSI Design VLSI Technology	Sex Physics Theory IA Theory IA Theory IA	Credits 4 2 4 2 4 2 4 2	Grad Obtai BC CC AB CC CC CC	de ned + + + + + +	FAILS NAIK MILIND MINANATH	
o Of Attempts: 4 Solid State Devices and Semiconductor Digital VLSI Design Analog VLSI Design	Sex Physics Theory IA Theory IA Theory IA Theory IA	Credits 4 2 4 2 4 2 4 2 4 2 4 2 4	Grad Obtai BC CC AB CC CC CC CC	de ned + + + + + + + + + + +	FAILS NAIK MILIND MINANATH	
o Of Attempts: 4 Solid State Devices and Semiconductor Digital VLSI Design Analog VLSI Design VLSI Technology	Sex Physics Theory IA Theory IA Theory IA Theory	Credits 4 2 4 2 4 2 4 2 4 2 4 2 4 2 4	Grad Obtai BC CC AB CC CC CC CC FF	de ned + + + + + + + +	FAILS NAIK MILIND MINANATH	
o Of Attempts : 4 Solid State Devices and Semiconductor Digital VLSI Design Analog VLSI Design VLSI Technology Advanced Engineering Mathematics	Sex Physics Theory IA Theory IA Theory IA Theory IA	Credits 4 2 4 2 4 2 4 2 4 2 4 2 4 2	Grad Obtai BC CC AB CC CC CC CC FF	de ned + + + + + + + + + + +	FAILS NAIK MILIND MINANATH	
o Of Attempts: 4 Solid State Devices and Semiconductor Digital VLSI Design Analog VLSI Design VLSI Technology	Sex Physics Theory IA Theory IA Theory IA Theory IA Theory IA	C: M No Of Credits 4 2 4 2 4 2 4 2 4 2 4 2 4 2 4 2 4 2	Grad Obtai BC CC AB CC CC CC CC FF CC	de ned + + + + + + F	FAILS NAIK MILIND MINANATH	
o Of Attempts : 4 Solid State Devices and Semiconductor Digital VLSI Design Analog VLSI Design VLSI Technology Advanced Engineering Mathematics	Sex Physics Theory IA Theory IA Theory IA Theory IA Theory IA IA	C: M No Of Credits 4 2 4 2 4 2 4 2 4 2 4 2 4 2 4 2 4 2 2	Grad Obtai BC CC AB CC CC CC CC FF CC CC	de ned + + + + + + F	FAILS NAIK MILIND MINANATH	
<ul> <li>Of Attempts : 4</li> <li>Solid State Devices and Semiconductor</li> <li>Digital VLSI Design</li> <li>Analog VLSI Design</li> <li>VLSI Technology</li> <li>Advanced Engineering Mathematics</li> <li>HDL - LAB</li> </ul>	Sex Physics Theory IA Theory IA Theory IA Theory IA Theory IA	C: M No Of Credits 4 2 4 2 4 2 4 2 4 2 4 2 4 2 4 2 4 2	Grad Obtai BC CC AB CC CC CC CC FF CC	de ned + + + + + + + + F +	FAILS NAIK MILIND MINANATH	
o Of Attempts : 4 Solid State Devices and Semiconductor Digital VLSI Design Analog VLSI Design VLSI Technology Advanced Engineering Mathematics	Sex Physics Theory IA Theory IA Theory IA Theory IA Theory IA IA Practical	C: M No Of Credits 4 2 4 2 4 2 4 2 4 2 4 2 4 2 2 2 2	Grad Obtai BC CC AB CC CC CC CC FF CC CC CC	de ned + + + + + + + + + + + + + + + + + + +	FAILS NAIK MILIND MINANATH	
<ul> <li>Of Attempts : 4</li> <li>Solid State Devices and Semiconductor</li> <li>Digital VLSI Design</li> <li>Analog VLSI Design</li> <li>VLSI Technology</li> <li>Advanced Engineering Mathematics</li> <li>HDL - LAB</li> </ul>	Sex Physics Theory IA Theory IA Theory IA Theory IA Theory IA IA Practical IA	<: M No Of Credits 4 2 4 2 4 2 4 2 4 2 4 2 4 2 2 2 2 2	Grad Obtai BC CC AB CC CC CC CC FF CC CC CC CC	de ned + + + + + + + + + + + + + + + + + + +	FAILS NAIK MILIND MINANATH	
<ul> <li>Of Attempts : 4</li> <li>Solid State Devices and Semiconductor</li> <li>Digital VLSI Design</li> <li>Analog VLSI Design</li> <li>VLSI Technology</li> <li>Advanced Engineering Mathematics</li> <li>HDL - LAB</li> </ul>	Sex Physics Theory IA Theory IA Theory IA Theory IA Theory IA IA Practical	C: M No Of Credits 4 2 4 2 4 2 4 2 4 2 4 2 4 2 2 2 2	Grad Obtai BC CC AB CC CC CC CC FF CC CC CC	de ned + + + + + + + + + + + + + + + + + + +	FAILS NAIK MILIND MINANATH	

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#### GOA UNIVERSITY Taleigao Plateau, Goa 403 206

RESULT REGISTER FOR M.E ELECTRONICS & TELECOMMUNICATION (MICROELECTRONICS) SEMESTER - I EXAMINATION HELD IN MAY 2016 Course : Revised Course - 2013

Seat No : 3105 P R No : 200800	0872 Sex	<: M	Nam	ne: NO	DRONHA ALWIN
No Of Attempts: 3		No Of Credits	Grad		SGPA
Solid State Devices and Semicono	luctor Physics				
	Theory	4	CC	+	
	IA	2	BB	+	
Digital VLSI Design					
	Theory	4	BC	P	
	IA	2	BB	+	
Analog VLSI Design					
	Theory	4	CC	+	
	IA	2	BB	+	
VLSI Technology					
	Theory	4	CC	+	
	IA	2	AB	+	
Advanced Engineering Mathematic					
	Theory	4	FF	F	
	IA	2	CC	+	
HDL - LAB					
	IA	2	BB	+	
	Practical	2	BC	+	
VLSI CAD - LAB					
	IA	2	AB	+	
	Practical	2	AB	+	
	Total :	38			5.53 F FAILS
Seat No : 3106 P R No : 20100	7394 Sex	<: M	Nam	ne: SH	HETGAONKAR ANAY ANIL
No Of Attempts: 2		No Of Credits	Grad		SGPA
Solid State Devices and Semicond	luctor Physics	orcuits	Obtai	incu	
	Theory	4	BC	P	
	IA	2	BC	+	
Digital VLSI Design					
	Theory	4	CC	P	
	IA	2	CC	+	
Analog VLSI Design					
	Theory	4	CC	+	
	ÍA	2	CC	+	
VLSI Technology					
	Theory	4	BC	Р	
	IA	2	CC	+	
Advanced Engineering Mathematic	cs				
	Theory	4	FF	F	
	IA	2	CC	+	
HDL - LAB					
	IA	2	BB	+	
	Practical	2	BB	+	
VLSI CAD - LAB					
	IA	2	BB	+	
	Practical	2	BC		

5.11 F

TALEIGAO

PLATEAU

GOA

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Prof. Y. V. Reddy Registrar

					FAILS
Grade	Grade Points	Performance	Read By : Pphile		
AO	10	Outstanding	Checked By KAL		
AA	9	Excellent	Checked by .		0
AB	8	Very Good	Date: 27716		No ando
BB	7	Good	Date 27 7 116	Star Bring to	macento
BC	6	Fair		26/2/2016	
CC	5	Satisfactory		S.S.J. Figueiredo	Leo V. Macedo
FF	0	Fail		Assistant Registrar-E(Proff.)	Controller Of Examinations
Dees		Teller A/ADC		AND AND MALE MALE FURTHER AN Oracles	Consider CODA, Comparison

Total :

P: Passes; F: Fails; A/ABS: Absent; N/NAP: Non Appearance; X/NE: Not Eligible; +: Grades Carried Over; SGPA: Semester Grade Point Average; CGPA: Cummulative Grade Point Average

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Discrepancy, if any in the Result shall be informed to the University within a week from the date of receipt of the result by the college Page 3 of 3 GO Tal

## GOA UNIVERSITY

Taleigao Plateau, Goa 403 206

RESULT REGISTER FOR M.E POWER & ENERGY SYSTEM ENGINEERING SEMESTER - I EXAMINATION HELD IN MAY 2016 Course : Revised Course - 2013

Seat No : 5101 P R No : 201510478	Se	x: M	Name	: /	ANUPAM SHARMA
lo Of Attempts: 2		No Of Credits	Grade		SGPA
Non Conventional Energy Systems					
	Theory	4	BB	Ρ	
	IA	2	BC	+	
Advanced Power Electronics					
	Theory	4	BC	+	
	IA	2	BB	+	
Computer Aided Power System Analysis					
	Theory	4	BB	+	
	IA	2	BB	+	
Optimization Techniques					
	Theory	4	BC	+	
	IA	2	BC	+	
DSP Application to Power System					
	Theory	4	BB	+	
	IA	2		+	
Power Engineering Lab - I					
5	IA	2	BC	+	
	Practical	4		+	
	Total :	36			6.61 P
	rotar.	00			PASSES
Seat No : 5102 P R No : 201007302	Se	x: M	Name	: .	ATAIDE LEANDER JONATHAN
No Of Attempts: 2		No Of	Grade	è	
		Credits	Obtain		SGPA
Non Conventional Energy Systems					v s s s s
	Theory	4	CC	Ρ	
	IA	2	CC	+	
Advanced Power Electronics					
	Theory	4	NAP	N	
	IA	2	BB	+	
Computer Aided Power System Analysis					
	Theory	4	NAP	N	
	IA	2	BC	+	
Optimization Techniques					
	Theory	4	BB	Ρ	
	IA	2	BC	+	
DSP Application to Power System					
	Theory	4	BC	Р	
	IA	2		+	
Power Engineering Lab - I					
Fower Engineering Lab - I		2	DD	+	
Fower Engineering Lab - I	IA	2	DD		
Fower Engineering Lab - 1	IA Practical	2 4		+	
					4.89 F FAILS

# COLLEGE : GOA COLLEGE OF ENGINEERING

Read By : pphilos Checked By : KM Grade Grade Performance Read By : Points 10 AO Outstanding AA 9 Excellent 7/16 AB 8 Very Good Date : 2 BB 7 Good BC 6 Fair CC 5 Satisfactory FF 0 Fail

2016 26/21

S.S.J. Figueiredo Assistant Registrar-E(Proff.)

116 F

Leo V. Macedo Controller Of Examinations

8 Y. V. Reddy Prof Registrar

P: Passes; F: Fails; A/ABS: Absent; N/NAP: Non Appearance; X/NE: Not Eligible; +: Grades Carried Over; SGPA: Semester Grade Point Average; CGPA: Cummulative Grade Point Average

Discrepancy, if any in the Result shall be informed to the University within a week from the date of receipt of the result by the college Page 1 of 1